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JAN 17 2006

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of

Hocavar (TI-36160)

Serial No. 10/806,879

Group Art Unit:

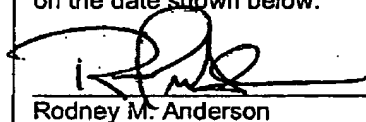
Filed: March 23, 2004

Examiner:

For: Layered Low Density Parity Check Decoding for Digital Communications

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Rodney M. AndersonJanuary 17, 2006  
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<input checked="" type="checkbox"/> <b>FACSIMILE COVER SHEET</b>	<input checked="" type="checkbox"/> <b>INFORMATION DISCLOSURE</b>
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<input type="checkbox"/> <b>DECLARATION</b>	<b>(4 Pages)</b>
<input type="checkbox"/> <b>ASSIGNMENT</b>	<input checked="" type="checkbox"/> <b>REFERENCE (12 pages)</b>
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PO Box 655474, M/S 3999  
Dallas, TX 75074

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PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of

Hocevar (TI-36160)

Serial No.10/806,879

Group Art Unit:

Filed: March 23, 2004

Examiner:

For: Layered Low Density Parity Check Decoding for Digital Communications

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Applicant brings the references listed on the enclosed PTO/SB/08 to the attention of the Patent and Trademark Office relative to the subject application.

On information and belief, no Office Action on the merits has yet been received. As such, the filing of this Information Disclosure Statement is timely.<sup>1</sup>

To the extent that the references are not U.S. Patents or U.S. Patent Publications,<sup>2</sup> copies are provided. All of the references are in the English language. As such, no additional statement of relevance is provided in this paper.

By citing these references, Applicant does not admit that any of these references is, or is considered to be, material to the patentability of any of the claims of this application.<sup>3</sup>

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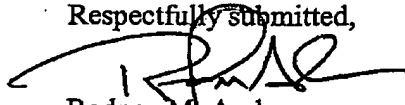
<sup>1</sup> 37 C.F.R. §1.97(b)(3).

<sup>2</sup> 37 C.F.R. §1.98(a), as amended effective October 21, 2004.

<sup>3</sup> 37 C.F.R. §1.97(h).

Consideration of this information in the prosecution of this application is respectfully requested.

Respectfully submitted,



Rodney M. Anderson

Registry No. 31,939

Attorney for Applicant

Anderson, Levine & Lintel, L.L.P.

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37 C.F.R. 1.8

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PTO/SB/088 (07-05)

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Substitute for form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		<b>Complete if Known</b>	
		Application Number	10/806,879
		Filing Date	March 23, 2004
		First Named Inventor	Hocevar
		Art Unit	
		Examiner Name	
Sheet 2 of 2	Attorney Docket Number	TI-36160	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		ZHANG et al., "VLSI Implementation-Oriented (3,k)-Regular Low-Density Parity-Check Codes", IEEE Workshop on Signal Processing Systems (Sept.2001), pp.25-36.	

Examiner Signature	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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